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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10029571	FILING DATE 12/20/2001	CLASS 702	SUBCLASS 119	GAU 2857	EXAMINER TSA
**APPLICANTS: Takahashi Eiji; Fukumoto Yukihiro; Saito Yoshiyuki; Shibata Osamu; Tanimoto Shinichi; Nakayama Takeshi;					
**CONTINUING DATA VERIFIED:					
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** FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-002179 01/10/2001					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's initials		NAK1-BQ83			
TITLE : Multilayer board in which wiring of signal line that requires tamper-resistance is covered by component or foil, design apparatus, method, and program for the multilayer board, and mediu					
<small>U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED		
			Total Claims	Print Claim for O.G.	
ISSUE FEE		Primary Examiner	DRAWING		
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.	Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner		
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